

Notice of Allowability

Application No.

10/804,974

Examiner

Hieu P. Nguyen

Applicant(s)

CANILAO ET AL.

Art Unit

2817

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to 03/18/2004.
2. ☒ The allowed claim(s) is/are 1-8.
3. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

- | | |
|--|--|
| 1. <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 5. <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 2. <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 6. <input type="checkbox"/> Interview Summary (PTO-413),
Paper No./Mail Date _____. |
| 3. <input checked="" type="checkbox"/> Information Disclosure Statements (PTO-1449 or PTO/SB/08),
Paper No./Mail Date _____ | 7. <input type="checkbox"/> Examiner's Amendment/Comment |
| 4. <input type="checkbox"/> Examiner's Comment Regarding Requirement for Deposit
of Biological Material | 8. <input checked="" type="checkbox"/> Examiner's Statement of Reasons for Allowance |
| | 9. <input type="checkbox"/> Other _____. |

DETAILED ACTION

Response to Arguments

Applicant's arguments, filed on 11/10/2005, have been fully considered and are persuasive. The rejection of claim 1,2 and 5-7 has been withdrawn.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Yamamoto et al. U.S. 6,707,341 discloses in Fig. 1 a power amplifier comprising: a first and second power transistor stages receiving first and second collector supply voltages. The first power transistor stage having a base biased by a first bias voltage, and the second power transistor stage having a base biased by a second bias voltage. The first stage bias circuit including a first and second bias transistors and the second bias circuit including third and fourth bias transistors, and the collector of the second and fourth bias transistors being bias by a **third supply voltage** (V_{cc}). Yamamoto further discloses another **supply voltage** (V_{pc}) connecting to the **gates** of the first and third bias transistors, but lacks to show "**collectors** of the first (Trb12) and third bias transistors (Trb22) operating in response to a fourth supply voltage".

Rahn et al. U.S. 6,518,840 discloses in Fig. 3 a power amplifier comprising: a first and second power transistor stage receiving first and second collector supply

voltages. The first power transistor stage having a base biased by the first bias voltage, and the second power transistor stage having a base bias by a second bias voltage. The first stage bias circuit including a first and second bias transistors and the second bias circuit including third and fourth bias transistors, and the collector of the second and fourth bias transistors being bias by a third supply voltage (VCC0), but fails to fairly show "the collector of the first and third bias transistors operating in response to a fourth supply voltage."

Allowable Subject Matter

Claims 1-8 are allowed.

The following is an examiner's statement of reasons for allowance:

Claims 1-8 are allowed over the prior art of record. The prior art of record, considered individually or in combination, fails to fairly teach or suggest the claimed circuit comprising, among other limitations and unobvious limitations of **"collectors of the first and third bias transistors operating in response to a fourth supply voltage"** structurally and functionally interconnected with other limitations in the manner as cited in the claims.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Hieu P. Nguyen whose telephone number is 571-272-8577. The examiner can normally be reached on M-F: 8 A.M - 5P.M.


If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Pascal can be reached on 571-272-1769. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Hieu Nguyen
AU: 2817

hn

Robert Pascal
Primary Examiner



Robert Pascal
Supervisory Patent Examiner
Technology Center 2800